

Low Power And Reliable Sram Memory Cell And Array Design

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Preface.- Introduction.- Fundamentals of SRAM Memory Cell.- Electrical Stability.- Sensitivity Analysis.- Memory Cell Design Technique for Low Power SOC.- Array Design Techniques.- Dummy Cell Design.- Reliable Memory Cell Design.- Future Technologies EAN/ISBN : 9783642195686 Publisher(s): Springer, Berlin Discussed keywords: CMOS-Schaltungen, SRAM (Static Random Access Memory) Format: ePub/PDF Author(s): Ishibashi, Koichiro - Osada, Kenichi

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