

# Advanced Model Order Reduction Techniques In Vlsi Design

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Systematic introduction to key model order reduction techniques in linear circuits, using real-world examples to illustrate advantages and disadvantages. Model order reduction (MOR) techniques reduce the complexity of VLSI designs, paving the way to higher operating speeds and smaller feature sizes. This book presents a systematic introduction to, and treatment of, the key MOR methods employed in general linear circuits, using real-world examples to illustrate the advantages and disadvantages of each algorithm. Following a review of traditional projection-based techniques, coverage progresses to advanced 'state-of-the-art' MOR methods for VLSI design, including HMOR, passive truncated balanced realization (TBR) methods, efficient inductance modeling via the VPEC model, and structure-preserving MOR techniques. Where possible, numerical methods are approached from the CAD engineer's perspective, avoiding complex mathematics and allowing the reader to take on real design problems and develop more effective tools. With practical examples and over 100 illustrations, this book is suitable for researchers and graduate students of electrical and computer engineering, as well as practitioners working in the VLSI design industry. EAN/ISBN : 9780511287091 Publisher(s): Cambridge University Press Format: ePub/PDF Author(s): Tan, Sheldon - He, Lei

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