

System-level Validation

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This book covers state-of-the art techniques for high-level modeling and validation of complex hardware/software systems, including those with multicore architectures. Readers will learn to avoid time-consuming and error-prone validation from the comprehensive coverage of system-level validation, including high-level modeling of designs and faults, automated generation of directed tests, and efficient validation methodology using directed tests and assertions. The methodologies described in this book will help designers to improve the quality of their validation, performing as much validation as possible in the early stages of the design, while reducing the overall validation effort and cost. EAN/ISBN : 9781461413592 Publisher(s): Springer, Berlin, Springer, New York Discussed keywords: Embedded Systems, Modellierung, System-on-Chip (SoC), SystemC Format: ePub/PDF Author(s): Chen, Mingsong - Qin, Xiaoke - Mishra, Prabhat

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